

2020-21

**B.V.V. Sangha's**  
**AMRUTA INSTITUTE OF ENGINEERING AND MANAGEMENT SCIENCES**  
**Department of Electronics and Communication Engineering**  
**TIME TABLE FOURTH SEM--2021**

IV SEMESTER

SCHEME -2018

DATE:13-04-2021

Days	9:00 to 9:55	9:55 to 10:50	10:50 to 11:00	11:00 to 11:55	11:55 to 12:50	12:50 to 1:15	1:15 to 2:15	2:15 to 3:15	3:15 to 4:15	
MON	AC	M 4	TEA BRE AK	CS	SS TUTORIAL	L B U R N E C A H K	LA	MC	AC TUTORIAL	
TUE	M4	C S		LA	SS		AC	CS TUTORIAL	MC	
WED	CS	S S		M4	MC		SS	LA	MC TUTORIAL	
THU	MC	L A		AC	M4		MC LAB			
FRI	SS	M 4		LA	CS		AC LAB			
SAT	MC	S S		CS	M4 TUTORIAL		AC	LA TUTORIAL	CPH	

Code	Subject name		Staff Name
18MAT41	Engg Mathematics-IV	M-4	Mrs.SMITHA PATIL/ARADHANA KRISHNA
18EC42	analog circuits	AC	Dr.VINAY KUMAR
18EC43	Control Systems	CS	Mrs. ANITHA N
18EC44	engineering statistics and linear algebra	ES&LA	Mr.ABHISHEK
18EC45	signals and systems	SS	Mr.MADHUSUDHAN S
18EC46	micro controller	MC	Mr.ANIL KUMAR K
18ECL47	micro controller lab	MC	Mr.ANIL KUMAR K
18ECL48	analog circuits lab	AC LAB	Dr.VINAY KUMAR
18CPH49	CPH	CPH	Mr.Shivaramu

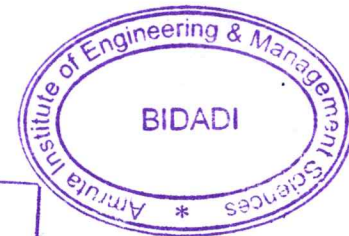
*Aradhana Krishna*  
**TIME TABLE CO-ORDINATOR**

*Vinay Kumar*  
**HOD** 17/04/21

*Aradhana Krishna*  
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**Department of Electronics and Communication Engineering**  
**TIME TABLE SIXTH SEM--2021**

**VI SEMESTER**

**SCHEME -2018 REGULAR**

**DATE:13-04-2021**

Days	9:00 to 9:55	9:55 to 10:50	10:50 to 11:00	11:00 to 11:55	11:55 to 12:50	12:50 to 1:15	1:15 to 2:15	2:15 to 3:15	3:15 to 4:15	
MON	M&A	M A D	TEA BRE AK	PYTHON	DC	L U N C H  B R E A K	ES TUTORIAL	MINI PROJECT		
TUE	ES	PYTHON		M A D	DC		ES LAB			
WED	PYTHON	M&A		M A D	ES		C O M M L A B			
THU	DC	ES		M&A	PYTHON TUTORIAL		M A D T U	DC TUTORIAL	MINI PROJECT	
FRI	M&A	DC		PYTHON	ES		M A D	DEPT ACTIVITY		
SAT	M&A	PYTHON		M&A TUTORIAL	M A D		DC	ES	SPORTS	

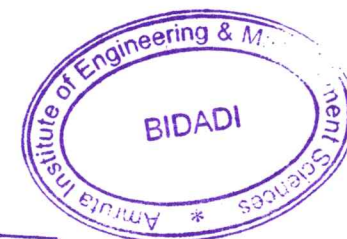
Code	Subject name		Staff Name
18EC61	Digital communication	DC	Mrs. ANITHA N
18EC62	Embedded systems ARM	ES	Mr.MADHUSUDHAN S
18EC63	Microwave and antenna	M & A	Mr.ABHISHEK
18EC646	Python	Python	Mr. chethan kumar
18CS651	Mobile application development	MAD	Mr.Prasanna
18ECL66	ARM LAB	ES	Mr.MADHUSUDHAN S
18ECL67	Communication lab	COM	Mrs. ANITHA N
18ECMP68	Mini project	MP	Mr.ANILKUMAR K

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**TIME TABLE EIGHTH SEM--2021**

**VIII SEMESTER**

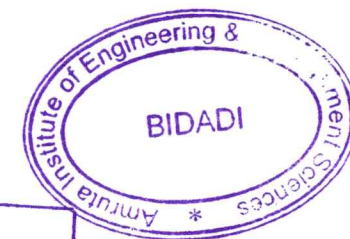
**SCHEME -2017 REGULAR**

**DATE:13-04-2021**

Days	9:00 to 9:55	9:55 to 10:50	10:50 to 11:00	11:00 to 11:55	11:55 to 12:50	12:50 to 1:15	1:15 to 2:15	2:15 to 3:15	3:15 to 4:15
MON	FON	M L	TEA BRE AK	LTE	FON	L U N C H	LTE TUTORIAL	SEMINAR	
TUE	ML	LTE		F O N	LTE		L TUTORIAL	SEMINAR	
WED	ML	LTE		F O N	ML TUTORIAL		F O N	D A / I N E R N	
THU	<----- PROJECT WORK ----->								
FRI	<----- PROJECT WORK ----->								
SAT	<----- PROJECT WORK ----->								

Code	Subject name		Staff Name
17EC81	Wireless comm &LTE	LTE	Dr. VEERESH PATIL
17EC82	Fiber optic networks	FON	Mr. CHETHANA KUMARA
17EC834	Mechine learning	ML	Mr.ANIL KUMAR K
17EC84	Internship	INTERN	Mr.MADHUSUDHAN S
17ECP85	Projectwork	PW	Mr.MADHUSUDHAN S
17ECS86	Seminar	SEMI	Mr.MADHUSUDHAN S

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**TIME TABLE SIXTH SEM--2021**

**VI SEMESTER**

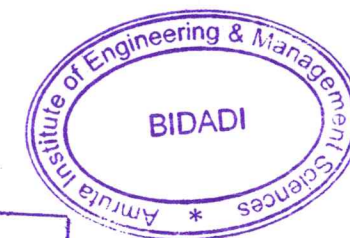
**SCHEME -2017 PARALLEL**

**DATE:13-04-2021**

Days	9:00 to 9:55	9:55 to 10:50	10:50 to 11:00	11:00 to 11:55	11:55 to 12:50	12:50 to 1:15	1:15 to 2:15	2:15 to 3:15	3:15 to 4:15	
MON	VLSI	C M C	TEA BRE AK	CCN	DC	L B U R N E C A H K	ES TUTORIAL	DSDV	DSDV	
TUE	ES	CCN		C M C	DC		ES LAB			
WED	CCN	VLSI		C M C	ES		C C N L A B			
THU	DC	ES		VLSI	CCN TUTORIAL		C M C	DC TUTORIAL	DSDV	
FRI	VLSI	DC		CCN	ES		C M C T U	DSDV	DSDV TUTORIAL	
SAT	DSDV	CCN		VLSI TUTORIAL	C M C		DC	ES	VLSI	

Code	Subject name		Staff Name
17EC61	Digital communication	DC	Mrs. ANITHA N
17EC62	Embedded systems ARM	ES	Mr.MADHUSUDHAN S
17EC63	VLSI	VLSI	Mr.ANIL KUMAR K
17EC64	Computer comm network	CCN	Mr.RAGHUNANDAN G
17EC651	Cellular mobile comm	CMC	Dr. VEERESH PATIL
17EC663	DSDV	DSDV	Mr.MADHUSUDHAN S
17ECL67	ARM LAB	ES	Mr.MADHUSUDHAN S
17ECL68	CCN LAB	CCN	Mr. CHETHANA KUMARA/Mr.RAGHUNANDAN

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**TIME TABLE EIGHTH SEM--2021**

**VIII SEMESTER**

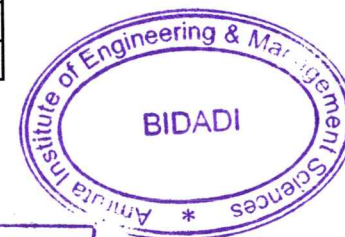
**SCHEME -2015 PARALLEL**

**DATE:13-04-2021**

Days	9:00 to 9:55	9:55 to 10:50	10:50 to 11:00	11:00 to 11:55	11:55 to 12:50	12:50 to 1:15	1:15 to 2:15	2:15 to 3:15	3:15 to 4:15
<b>MON</b>	FON	M L	TEA BRE AK	LTE	FON	L U N C H	LTE TUTORIAL	SEMINAR	
<b>TUE</b>	ML	LTE		F O N	LTE		L T U R I A L	SEMINAR	
<b>WED</b>	ML	LTE		F O N	ML TUTORIAL		F O N	D / N R A I E N	
<b>THU</b>	←----- PROJECT WORK ----->								
<b>FRI</b>	←----- PROJECT WORK ----->								
<b>SAT</b>	←----- PROJECT WORK ----->								

Code	Subject name		Staff Name
<b>15EC81</b>	Wireless comm &LTE	LTE	Dr. VEERESH PATIL
<b>15EC82</b>	Fiber optic networks	FON	Mr. CHETHANA KUMARA
<b>15EC834</b>	Mechine learning	ML	Mr.ANIL KUMAR K
<b>17EC84</b>	Internship	INTERN	Mr.MADHUSUDHAN S
<b>17ECP85</b>	Projectwork	PW	Mr.MADHUSUDHAN S
<b>17ECS86</b>	Seminar	SEMI	Mr.MADHUSUDHAN S

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**TIME TABLE THIRD SEM--2020**      CONTACT CLASS TT

**III SEMESTER**

Days	9:00-9.55	9:55-10:50	10:50-11:00	11:00-11:55	11:55-12:50	12.50 - 1:15	1.15-2.10	2.10-3.05	3.05-4.00	
MON	NT	DSD Lab				LUNCH BREAK	ED&I LAB			
TUE	M-3	ED&I LAB					DSD LAB			
WED	ED	PE&I		ED	M-3		DSD	CO&A	DSD	
THU	CO&A	DSD Lab					ED&I LAB			
FRI	PE&I	ED&I LAB					DSD Lab			
SAT	DSD	ED	BREAK	CO&A	NT		PE&I	KANNADA		

Code	Subject name		Staff Name
18MAT31	Engg Mathematics-III	M-3	Mrs.Aaradhana krishna
18EC32	Network theory	NT	Mr. RAGHUNANDAN G
18EC33	Electronic Devices	ED	Mrs. ANITHA N
18EC34	Digital system design	DSD	Dr. VEERESH PATIL
18EC35	Computer organisation and architecture	CO&A	Mr. ABHISHEK K H
18EC36	power electronics and instrumentation	PE&I	Mr. ANILKUMAR K
18ECL37	Electronic Devices and Instrumentation lab	ED&I LAB	Mrs. ANITHA N/Mr.Mr. ABHISHEK K H
18ECL38	Digital system design lab	DSD LAB	Mr. ABHISHEK K H/Mrs. ANITHA N
18KAK39/18KEK39	Kannada	KANNADA	Mr.Shivaramu

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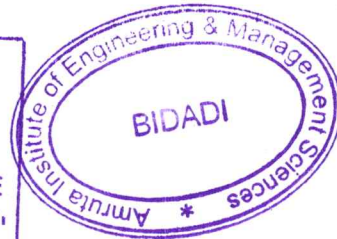
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**TIME TABLE FIFTH SEM--2020**  
**V SEMESTER**

CONTACT CLASS TT

Days	9:00-9.55	9:55-10:50	10:50-11:00	11:00 - 11:55	11:55-12:50	12.50 - 1:15	1.15-2.10	2.10-3.05	3.05-4.00	
MON	PCS	DSP LAB				LUNCH BREAK	HDL LAB			
TUE	ITC	EMW	Break	TI,M&E	HDL		TI,M&E	PCS	SPORTS	
WED	EMW	HDL LAB					DSP LAB			
THU	HDL	ITC	Break	DSP	PCS		HDL	EMW	TI,M&E	
FRI	HDL	ITC	Break	ITC	DSP		DSP	EMW	TI,M&E	
SAT	EMW	PCS	Break	HDL	DSP		ITC	DEPT ACTIVITY		

Code	Subject name		Staff Name
18ES51	Technical Innovation, Management & Entrepreneurship	TI,M&E	Dr. Vinay Kumar
18EC52	Digital Signal Processing	DSP	Mr. MADHUSUDHAN S
18EC53	Principals of Communication System	PCS	Mr. CHETHANA KUMARA S
18EC54	Information Theory and Coding	ITC	Mrs. ANITHA N
18EC55	Electromagnetic Waves	EMW	Mr. RAGHUNANDAN G
18EC56	Verilog HDL	VHDL	Mr. ANILKUMAR K
18ECL57	Digital Signal Processing lab	DSP LAB	Mr. MADHUSUDHAN S/Dr. Vinay Kumar
18ECL58	HDL lab	HDL LAB	Mr. ANILKUMAR K/ Mr.CHETHANA KUMARA S

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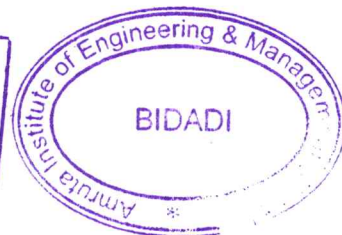
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**TIME TABLE SEVENTH SEM--2020** CONTACT CLASS TT

**VII SEMESTER**

Days	9:00-9.55	9:55-10:50	10:50-11:00	11:00-11:55	11:55-12:50	12.50 - 1:15	1.15-2.10	2.10-3.05	3.05-4.00
MON	M&A	PE	TEA BREAK	IOT	DIP	LUNCH BREAK	PROJECT WORK PHASE I		
TUE	DIP	PE		MMC	PE		IOT	LIBRARY	Sports
WED	M&A	MMC		PE	IOT		PROJECT WORK PHASE I		
THU	DIP	MMC		M&A	IOT		ADC LAB		
FRI	PE	DIP		M&A	MMC		VLSI LAB		
SAT	DIP	M&A		MMC	IOT		DEPT ACTIVITY		

Code	Subject name		Staff Name
17EC71	Microwave and Antennas	M&A	Ms. RASHMI KITTALI
17EC72	Digital Image Processing	DIP	Mr. MADHUSUDHAN S
17EC73	Power Electronics	PE	Dr. Vinay Kumar
17EC741	Multi Media Communication	MMC	Mr. CHETHANA KUMARA S
17EC752	IOT and Wireless Sensor Networks	IOT	Dr. VEERESH PATIL
17ECL76	Advance communication lab	ADC LAB	Mr. CHETHANA KUMARA S/Mr.RAGHUNANDHAN
17ECL77	VLSI lab	VLSI LAB	Dr. Vinay Kumar/ Mr.ANILKUMAR K
17ECP78	Project work phase -I + Project Work Seminar	PWP-1	Mr.MADHUSUDHAN S

*Shashu* 08/12/20  
 TIME TABLE CO-ORDINATOR

*Raghu* 08/12/2020  
 HOD

*Rmd*  
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 Bidadi Industrial Area, Near Toyota Kirloskar Motors  
 Bidadi, Bangalore-562 109.

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