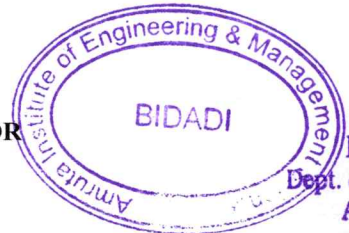


B V V Sangha's
Amruta Institute of Engineering and Management Sciences
Department of ECE, Master Time Table ODD SEMSTER 2018 -19

Day/ Time	SEM	8.30-9.25	9.25-10.20	10.20-10.40	10.40-11.35	11.35-12.30	12.20-1.00	1.00-1.55	1.55-2.50	2.50-3.45
Mon	IV	M4	LIC	TEA BREAK	MP	PCS	LUNCH BREAK	SS	CS	SC
	VI	DC	ARM		DSDV	VLSI		CCN	SC	
	VIII	FON	WC		NS	WC		← Seminar →		
Tue	IV	PCS	MP		M4	LIC		← MP LAB →		
	VI	ARM	DSDV		VLSI	CCN		CMC	DC	SC
	VIII	WC	NS		FON	NS		SC		
Wed	IV	MP	SS		M4	CS		LIC	SS	CPH
	VI	VLSI	DC		ARM	CCN		← ARM LAB →		
	VIII	NS	FON		WC	FON		Seminar		Dept. activity
Thu	IV	LIC	PCS		M4	CS		← LIC LAB →		
	VI	CMC	ARM		DC	DSDV		← CCN LAB →		
	VIII	PROJECT WORK			PROJECT WORK					
Fri	IV	M4	CS		SS	PCS		MP	LIC	Dept. activity
	VI	CCN	CMC		VLSI	DC		DSDV	CMC	
	VIII	PROJECT WORK		PROJECT WORK						
Sat	IV	CS	PCS	M4	MP	SS				
	VI	DSDV	CCN	ARM	CMC	VLSI				
	VIII	PROJECT WORK		PROJECT WORK						

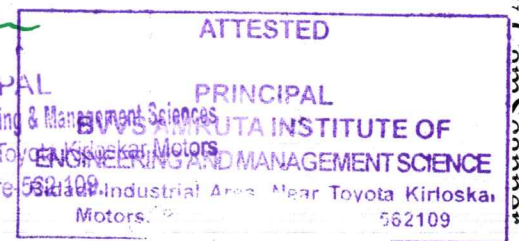
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TIME TABLE COORDINATOR



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HOD
 Head of the Department
 Dept. of Electronics & Communication Engg
 Amruta Institute of Engineering &
 Management Sciences
 Bidadi, Off Mysore Road, Bangalore 562 109

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B V V Sangha's
Amruta Institute of Engineering and Management Sciences
 Department of ECE, Master Time Table ODD SEMSTER 2018 -19

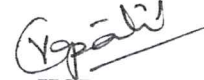
Day/ Time	SEM	8.30-9.25	9.25-10.20	10.20-10.40	10.40-11.35	11.35-12.30	12.20-1.00	1.00-1.55	1.55-2.50	2.50-3.45
Mon	IV	M4	LIC	TEA BREAK	MP	PCS	LUNCH BREAK	SS	CS	SC
	VI	DC	ARM		DSDV	VLSI		CCN	SC	
	VIII	FON	WC		NS	WC		← Seminar →		
Tue	IV	PCS	MP		M4	LIC		← MP LAB →		
	VI	ARM	DSDV		VLSI	CCN		CMC	DC	SC
	VIII	WC	NS		FON	NS		SC		
Wed	IV	MP	SS		M4	CS		LIC	SS	CPH
	VI	VLSI	DC		ARM	CCN		← ARM LAB →		
	VIII	NS	FON		WC	FON		Seminar		Dept. activity
Thu	IV	LIC	PCS		M4	CS		← LIC LAB →		
	VI	CMC	ARM		DC	DSDV		← CCN LAB →		
	VIII	PROJECT WORK			PROJECT WORK					
Fri	IV	M4	CS	SS	PCS	MP	LIC	Dept. activity		
	VI	CCN	CMC	VLSI	DC	DSDV	CMC			
	VIII	PROJECT WORK		PROJECT WORK						
Sat	IV	CS	PCS	M4	MP	SS				
	VI	DSDV	CCN	ARM	CMC	VLSI				
	VIII	PROJECT WORK		PROJECT WORK						

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Amruta Institute of Engineering & Management Sciences
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 Dept. of Electronics & Communication Engg
 Amruta Institute of Engineering & Management Sciences
 Bidadi, Off Mysore Road, Bangalore 562 109


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 Bidadi Industrial Area, Near Toyota Kirloskar Motors
 Bidadi, Bangalore-562 109.

Ho/D

B.V.V. Sangha's
AMRUTA INSTITUTE OF ENGINEERING AND MANAGEMENT SCIENCES
Department of Electronics and Communication Engineering
TIME TABLE -2018

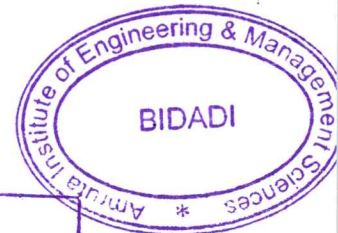
III SEMESTER

Days	8.30-9.25	9.25-10.20	10.20 - 10.40	10.40- 11.35	11.35- 12.30	12.30 - 1.00	1.00-1.55	1.55-2.50	2.50-3.45
MON	M3	AE	B R E A K	NA	EI	B R E A K	DE	EE	SC
TUE	NA	EI		M3	AE		← AE LAB →		
WED	EE	DE		M3	NA		AE	SC	Kannada
THU	M3	EE		EI	DE		← DE LAB →		
FRI	M3	AE		EI	EE		DE	NA	Departmental activity
SAT	AE	NA		M3	DE		EI	EE	

SC=Smart Class

Code	Subject name	Staff Intial	Staff Name
17MAT31	Engg mathematics-III	SH	Ms.Shobha
17EC32	Electronic Instrumentation	RS	Ms.Rashmi S
17EC33	Analog Electronics	AN	Mrs.AnithaN
17EC34	Digital Electronics	VP	Dr.Veeresh Patil
17EC35	Network Analysis	GB	Mrs.Geetha B
17EC36	Engg.Electromagnetics	CKS	Mr.Chethana kumar S
17ECL37	AEC LAB	MS+NF1	Mr.Madhusudhan S+New Faculty1
17ECL38	DE & Interfacing LAB	VP+NF2	Dr.Veeresh patil+New Faculty2

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AMRUTA INSTITUTE OF ENGINEERING AND MANAGEMENT SCIENCES
Department of Electronics and Communication Engineering
TIME TABLE -2018

V SEMESTER

Days	8.30-9.25	9.25-10.20	10.20 - 10.40	10.40- 11.35	11.35- 12.30	12.30 - 1.00	1.00-1.55	1.55-2.50	2.50-3.45
MON	OS	AUE	B R E A K	M&E	DSP	B R E A K	← DSP LAB →		
TUE	ITC	M&E		DSP	AUE		VHDL	OS	SC
WED	VHDL	ITC		DSP	AUE		← HDL LAB →		
THU	AUE	M&E		ITC	VHDL		OS	ITC	SC
FRI	DSP	VHDL		OS	M&E		ITC	AUE	Departmental activity
SAT	M&E	DSP		OS	VHDL				

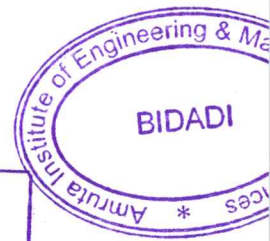
SC=Smart Class

Code	Subject name	Staff Intial	Staff Name
15EC51	Mng & entrepreneurship	VP	Dr.Veeresh Patil
15EC52	Digital signal processing	MS	Mr.Madhusudhan S
15EC53	Verilog HDL	AKK	Mr.AnilKumar
15EC54	Information theory& coding	NF1	New faculty1
15EC554	Operating Systems	NF2	New faculty2
15EC563	Automative Electronics	RS	Ms.Rashmi S
15ECL57	DSP LAB	MS+NF2	Mr.Madhusudhan S +New Faculty2
15ECL58	HDL LAB	AKK+CKS	Mr.AnilKumar+Mr.Chethana Kumar S

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B.V.V. Sangha's
AMRUTA INSTITUTE OF ENGINEERING AND MANAGEMENT SCIENCES
Department of Electronics and Communication Engineering
TIME TABLE -2018

VII SEMESTER

Days	8.30-9.25	9.25-10.20	10.20 - 10.40	10.40- 11.35	11.35- 12.30	12.30 - 1.00	1.00-1.55	1.55-2.50	2.50-3.45	
MON	MC	DIP	B R E A K	MA	PE	B R E A K	← ADC LAB →			
TUE	PE	MA		DSPA	MC		← VLSI LAB →			
WED	DIP	PE		MC	DSPA		DIP	SC		
THU	MA	DSPA		DIP	PE		← PROJECT PHASE1 →			
FRI	DSPA	MC		DIP	MA		PE	SC	Departmental activity	
SAT	MC	MA		PE	DSPA					

SC=Smart Class

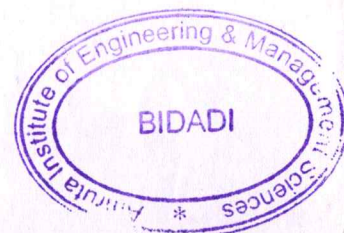
Code	Subject name	Staff Intial	Staff Name
15EC71	Microwave & Antenna	AKK	Mr.AnilKumar K
15EC72	Digital Image Processing	MS	Mr.Madhusudhan S
15EC73	Power electronics	AN	Mrs.AnithaN
15EC741	Miultimedia Communication	CKS	Mr.Chethana kumar S
15EC751	DSPA	CRD	Mr.Chittranjan Das
15ECL77	ADC LAB	CKS+AKK	Mr.Chethana Kumar S+ Mr.AnilKumar K
15ECL78	VLSI LAB	AN+RS	Mrs.Anitha N +Rashmi S

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HOD 23/08/18
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AMRUTA INSTITUTE OF ENGINEERING AND MANAGEMENT SCIENCES
Department of Electronics and Communication Engineering
TIME TABLE PARALLEL COURSE-2018

VII SEMESTER

Days	8.30-9.25	9.25-10.20	10.20 - 10.40	10.40- 11.35	11.35- 12.30	12.30 - 1.00	1.00-1.55	1.55-2.50	2.50-3.45
MON	ESD	IP	B R E A K	OFC	PE	B R E A K	← PE LAB →		
TUE	PE	OFC		DSPA	CCN		← VLSI LAB →		
WED	IP	PE		CCN	DSPA		IP	ESD	CCN
THU	OFC	DSPA		IP	PE		ESD	OFC	
FRI	DSPA	ESD		OFC	IP		PE	CCN	
SAT	CCN	ESD		PE	DSPA				

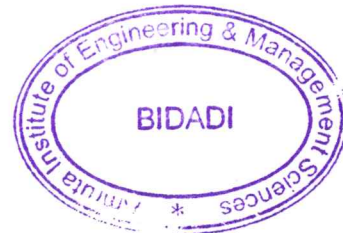
Code	Subject name	Staff Intial	Staff Name
10EC71	Computer communication	AKK	Mr.AnilKumar
10EC72	Optical fibre communication	CKS	Mr.Chethana kumar S
10EC73	Embedded system	RS	Mr.ASHOK K N
10EC74	Power Electronics	AN	Mrs.AnithaN
10EC751	DSPA	CRD	Mr.Chitharanjan das
10EC763	Image Processing	MS	Mr.Madhusudhan S
10ECL77	PE LAB	AN	Mrs.AnithaN
10ECL78	VLSI LAB	AN	Mrs.AnithaN

44
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Kpali
HOD



Name 30/07/18
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Department of Electronics and Communication Engineering
TIME TABLE EVEN SEM -2019

VIII SEMESTER

Days	8.30-9.25	9.25-10.20	10.20 - 10.40	10.40- 11.35	11.35- 12.30	12.30 - 1.00	1.00-1.55	1.55-2.50	2.50-3.45		
MON	FON	WC	B R E A K	NS	WC	B R E A K	← Seminar →				
TUE	WC	NS		FON	NS		SC				
WED	NS	FON		WC	FON		Seminar		Dept. activity		
THU	PROJECT WORK			PROJECT WORK			PROJECT WORK				
FRI	PROJECT WORK			PROJECT WORK							
SAT	PROJECT WORK			PROJECT WORK							


SC=Smart Class

Code	Subject name	Staff Intial	Staff Name
15EC81	Wireless Cellular <E 4G	VP	Dr.Veeresh Patil
15EC82	Fibre Optics & Networks	CKS	Mr.Chethana kumar S
15EC835	Network & Cyber security	AN	Mrs.AnithaN
15ECP858	Project Work	AN	Mrs.AnithaN
15ECS86	Seminar	AN	Mrs.AnithaN


TIME TABLE CO-ORDINATOR

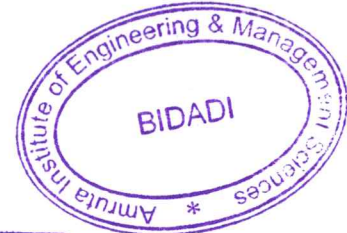

01/02/19
HOD

Head of the Department
Dept. of Electronics & Communication Engg
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Bidadi, Off Mysore Road, Bangalore - 562 109


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EVEN/8-19


B.V.V. Sangha's
AMRUTA INSTITUTE OF ENGINEERING AND MANAGEMENT SCIENCES
Department of Electronics and Communication Engineering
TIME TABLE - 2019- Even Semester (Parallel Course)


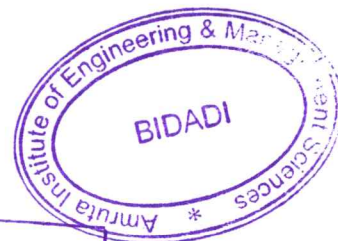
VIII SEMESTER

Days	8.30-9.25	9.25-10.20	10.20 - 10.30	10.30- 11.25	11.25- 12.20	12.20 - 1.00	1.00-1.55	1.55-2.50	2.50-3.45
MON	DSS	WC	B T R E E A A K	NS	WC	L B U R N E C A H K	GSM		
TUE	WC	NS		NS	DSS		GSM	SEMINAR	
WED	NS	DSS		WC	GSM		SC		Dept. activity
THU	PROJECT			PROJECT			PROJECT		
FRI	PROJECT			PROJECT			PROJECT		
SAT	PROJECT			PROJECT			PROJECT		

Code	Subject name	Staff Intial	Staff Name
10EC81	Wireless communication	VP	Dr.Veeresh Patil
10EC82	Digital switching systems	CKS	Mr.Chethana kumar S
10EC832	Network security	AN	Mrs.Anitha N
10EC843	GSM	CRD	Mr.Chittranjan Das
10ECP85	Project work	AN	Mrs.Anitha N
10ECS86	Seminar	AN	Mrs.Anitha N


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 Amruta Institute of Engineering &
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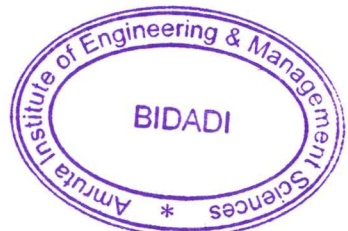
B V V Sangha's
Amruta Institute of Engineering and Management Sciences
Department of ECE, Master Time Table EVEN SEMSTER 2018

Day/ Time	SEM	8.30-9.25	9.25-10.20	10.20-10.30	10.30-11.25	11.25-12.20	12.20-1.00	1.00-1.55	1.55-2.50	2.50-3.45		
Mon	IV	MAT-4	LIC	TEA BREAK	CS	PCS	LUNCH BREAK	S&S	LIBRARY			
	VII	VLSI	DC		CMC	DSD		ARM	DC	VLSI		
	VIII	DSS	NS		GSM	WC		DSS	WC	NS		
Tue	IV	MAT-4	LIC		PCS	μP		μP LAB		DSD	LIBRARY	
	VII	DC	CMC		VLSI	CCN		GSM	SEMINAR			
	VIII	WC	GSM		NS	DSS						
Wed	IV	S&S	CS		MAT-4	μP		PCS	S&S	LIC	EMBEDDEDLAB	
	VII	CCN	DC		ARM	DSD		PROJECT				
	VIII	GSM	NS		WC	DSS		PROJECT				
Thu	IV	μP	PCS		MAT-4	S&S		LIC LAB		VLSI	SPORTS	
	VII	CMC	CCN		DSD	ARM		PROJECT				
	VIII	PROJECT										
Fri	IV	CS	LIC	MAT-4	CS	μP	PCS	SPORTS	CCN LAB			
	VII	ARM	VLSI	CCN	CMC	PROJECT						
	VIII	PROJECT										
Sat	IV	MAT-4	LIC	S&S	CS	μP	Tutorial Classes					
	VII	DSD	DC	ARM	CCN	CMC	Tutorial Classes					
	VIII	PROJECT						PROJECT				

P. Prasad
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C. R. ...
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M. ...
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